

FIG. 1 is a block diagram of a video coding system.

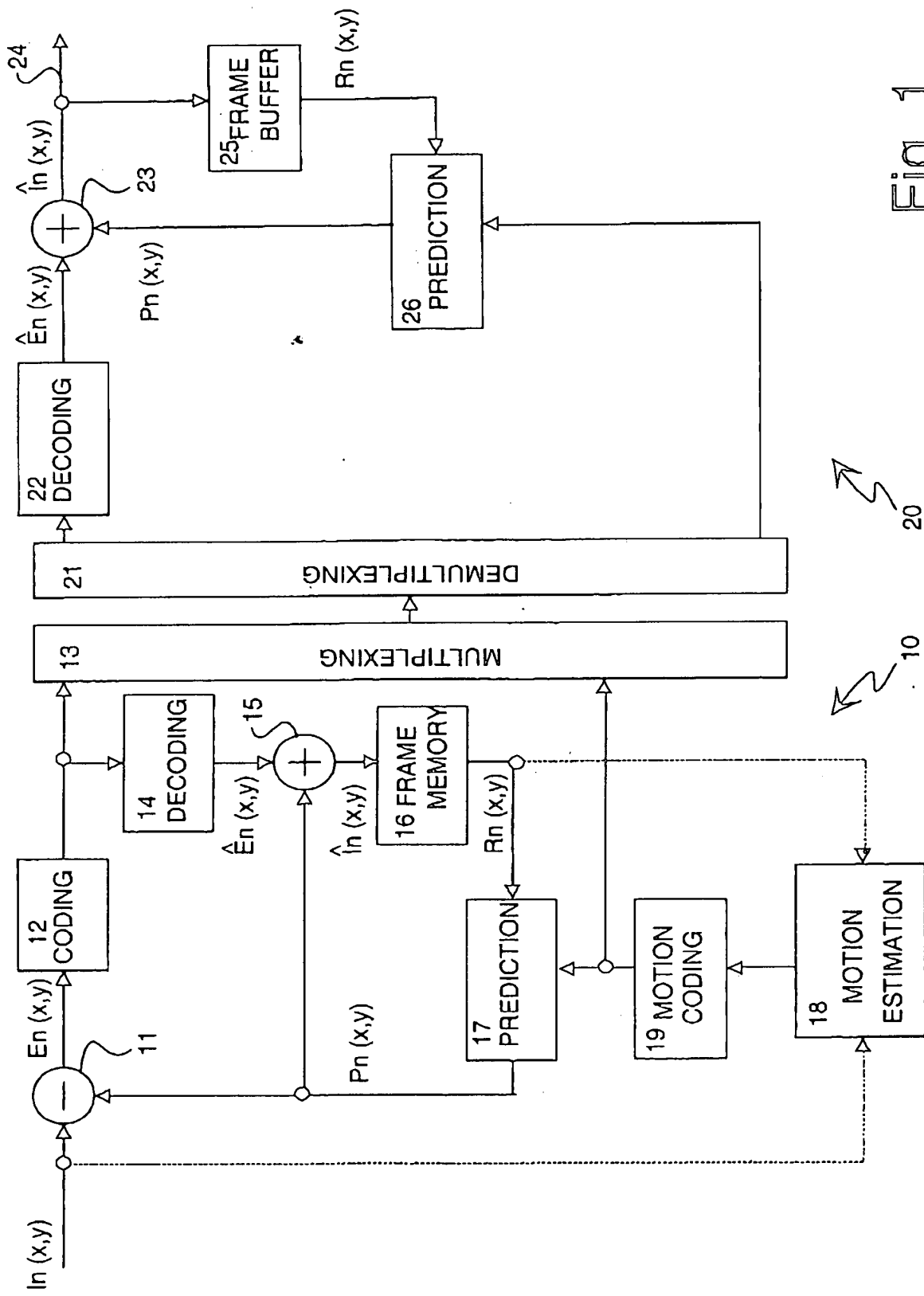


Fig. 1

FIG. 2 is a schematic diagram of the system of the present invention.

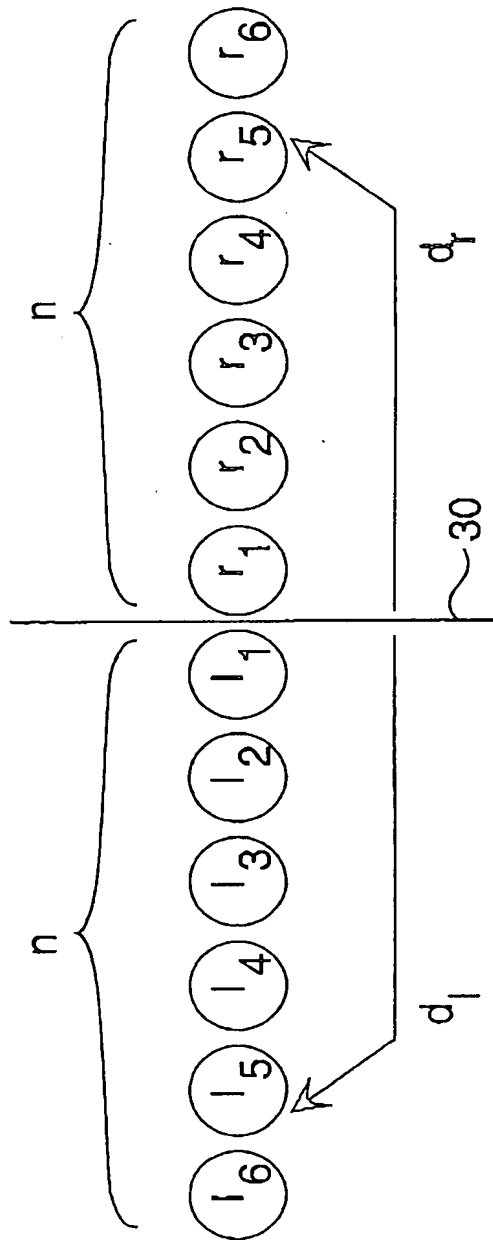


Fig. 2

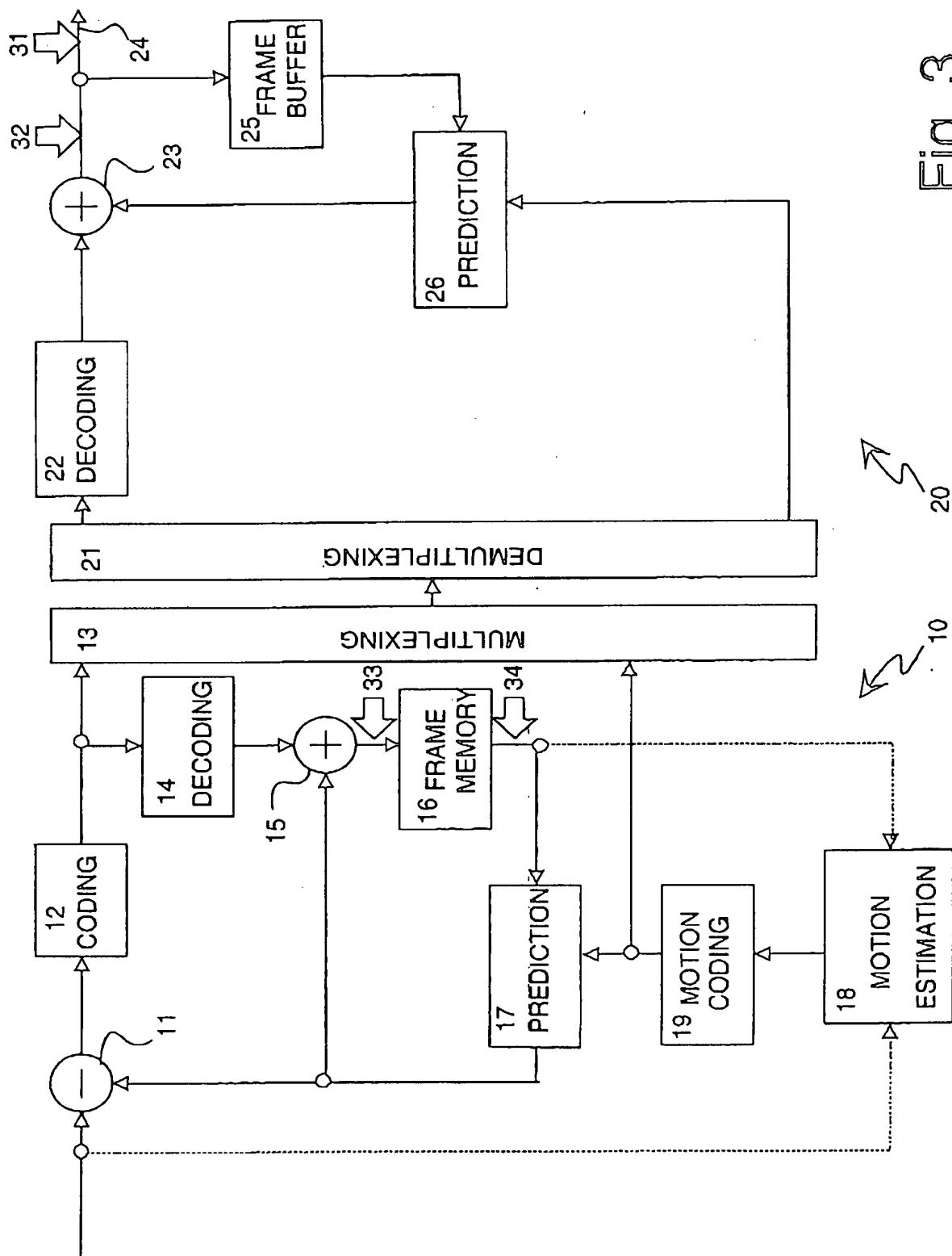


Fig. 3

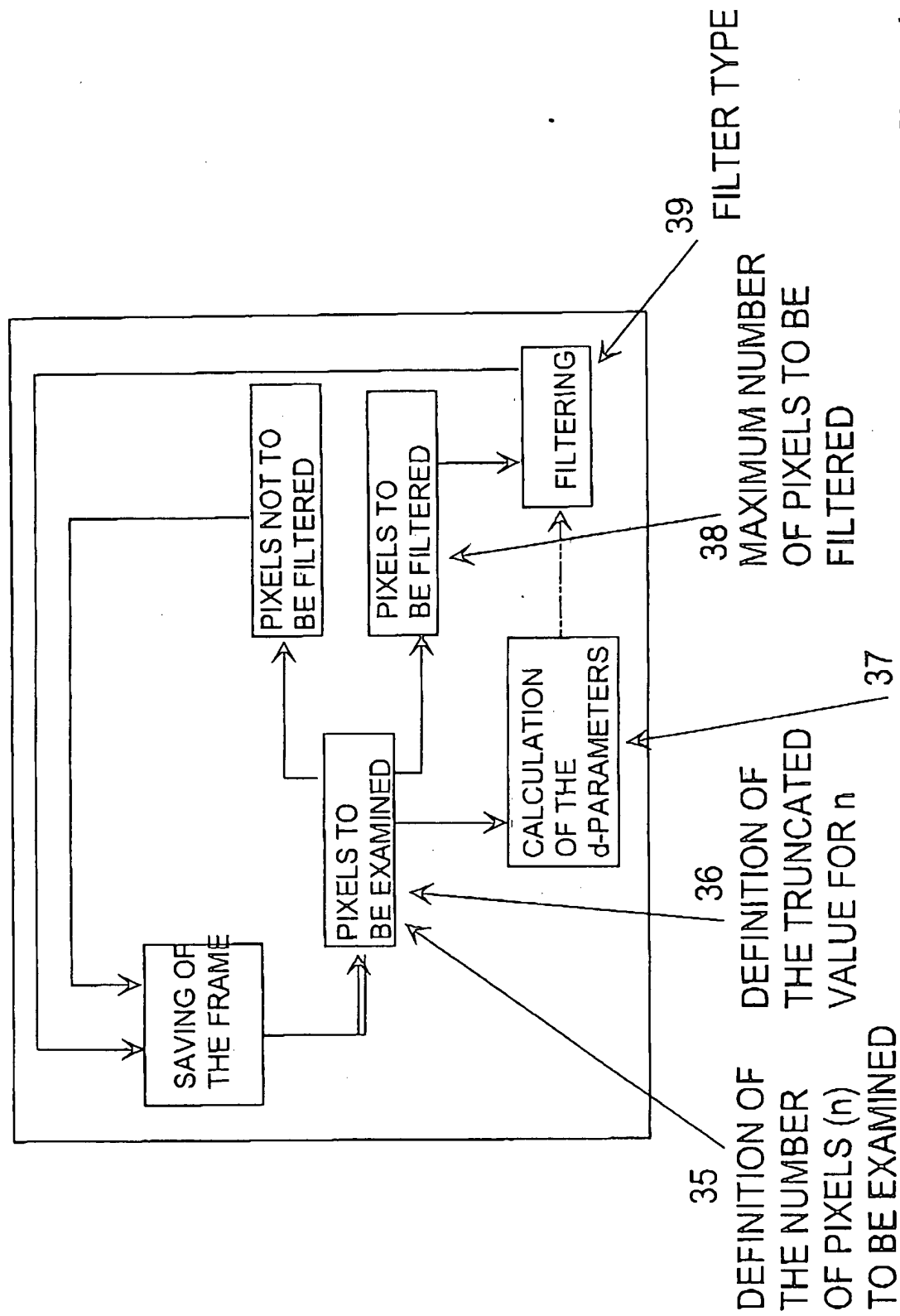
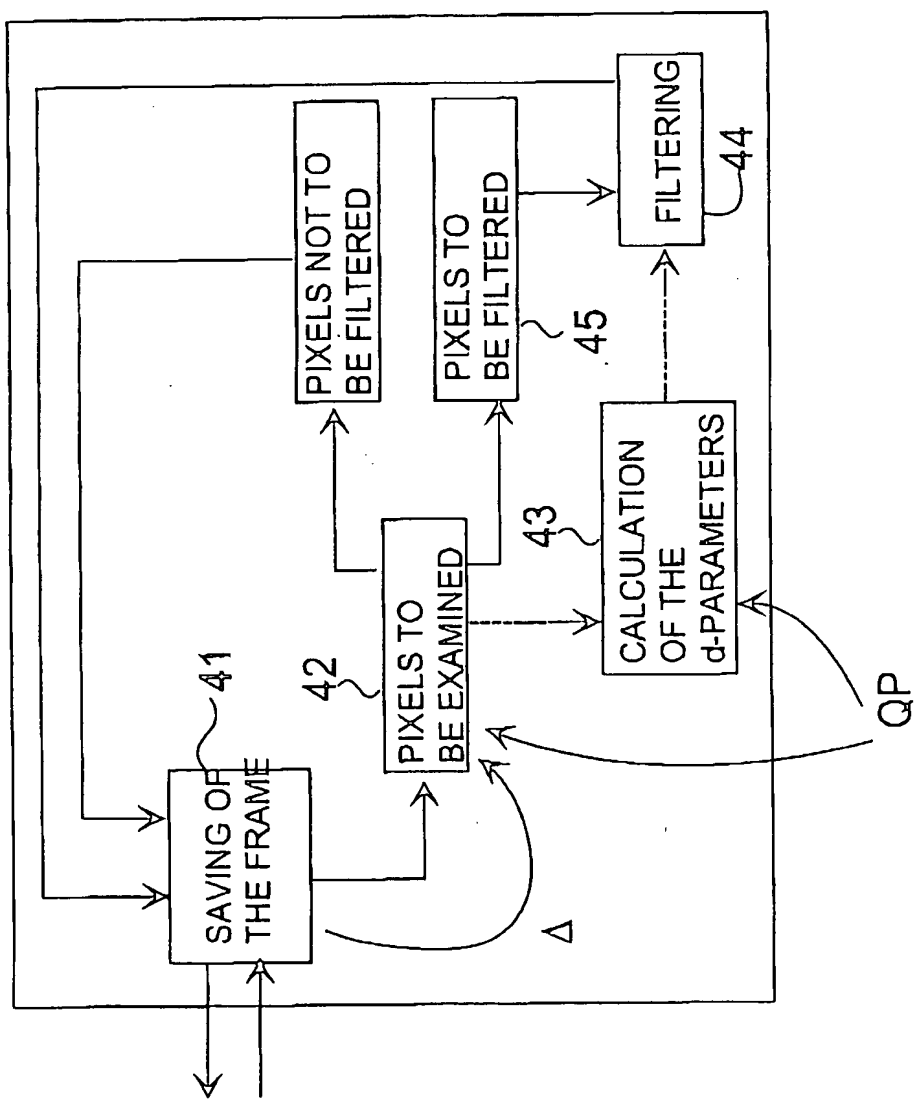


Fig. 4



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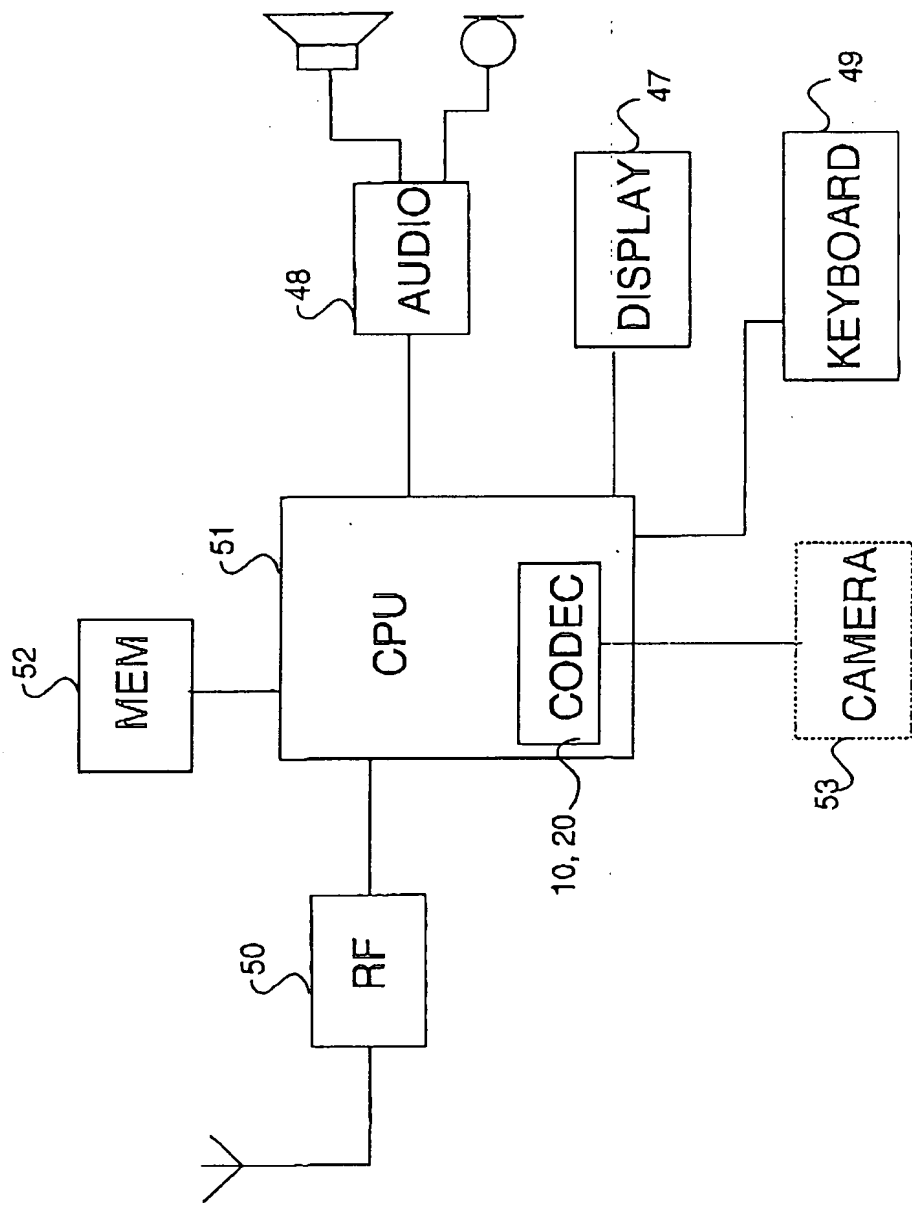


Fig. 6